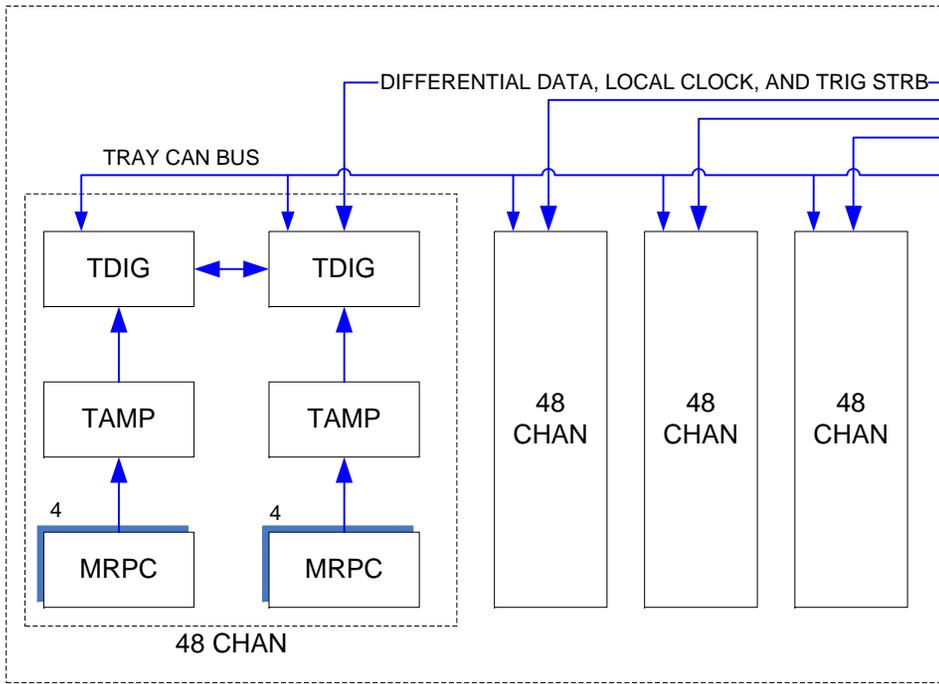
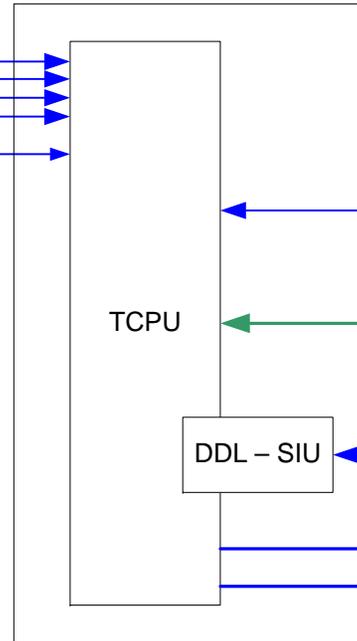


DETECTOR TRAY



STOP TCPU - CHASSIS 1



TRIGGER & RHIC CLK

SYSTEM CAN BUS

FIBER

LOCAL CLK & Rst

LOCAL CLK & Rst

TCD

TCPU

DDL - SIU

MYRANET

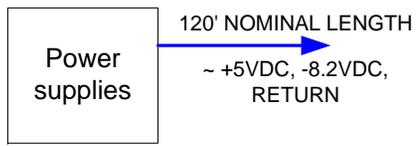
DAQ PC

48 CHAN

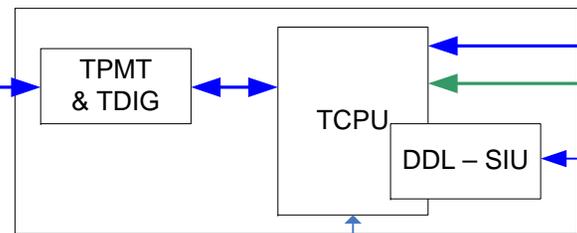
48 CHAN

48 CHAN

48 CHAN



START TCPU - CHASSIS 2



System CANbus

FIBER

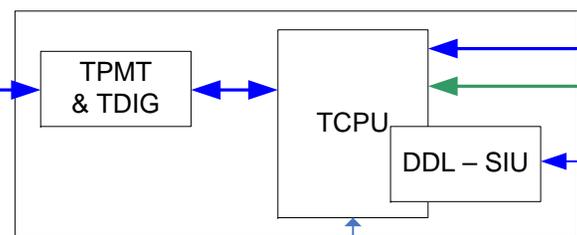
PMTs

TPMT & TDIG

TCPU

DDL - SIU

START TCPU - CHASSIS 3



System CANbus

FIBER

PMTs

TPMT & TDIG

TCPU

DDL - SIU

HOST PC

